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Guus Kleinlein	3745880	Rajeck Massa	2848457	Ch38 - Wireless Sensor Networks
Angelos Taxiarchis Kalaitzidis	4235789			Ch18 - Multiprocessor System-on-Chip Prototyping Using Dynamic Binary Translation
Diogo Santos	4143108	Alicia Grado Guerrero	4173406	Ch15 - Network-on-Chip Design
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Arnaud Saint-Genez	2624443			Ch6 - Optimization Strategies in Design Space Exploration
Xiaolin Gu	3790398	Ke Hou	3962830	Ch21 - Timing Models for Fast Embedded Software Performance Analysis
Amber Zitman	1569619			Ch4 - ForSyDe: System Design Using a Functional Language and Models of Computation